

Abstract of the Disclosure

A system and method for peripheral control. The present invention relates to utilizing device address call sequencing for control of active memory bus peripheral devices. To prevent problems inherent with triggering events in active memory bus peripherals, such as are associated with error correction means, and to minimize pin usage, a system and method are provided to utilize memory address call sequencing of other devices to trigger such events.

09888105-062201
T02290-50T8880